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In re Application for:

Quat T. Vu, et al.

Application No.: 09/884,595

Filed: June 18, 2001

For: MICROELECTRONIC SUBSTRATES WITH
INTEGRATED DEVICES

Examiner: Not Assigned

Art Group: 2826

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to:
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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, the following pending U.S. Patent Applications are related to the above-referenced U.S. Patent Application:

<u>Application No.</u>	<u>Filing Date</u>
09/438,221	11/12/1999
09/679,733	10/04/2000
09/658,819	09/08/2000
09/660,755	09/13/2000
09/660,757	09/13/2000
09/733,289	12/08/2000
09/738,117	12/15/2000
09/692,908	10/19/2000
09/691,738	10/18/2000
09/640,961	08/16/2000
09/817,710	03/26/2001


Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

If any additional fee is required, please charge Deposit Account No. 02-2666. A Fee Transmittal is enclosed in duplicate for deposit account charging purposes.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN, LLP

Date: Nov. 5, 2001



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